

**UNIVERSITY OF KERALA**  
**TIME TABLE FOR FIFTH SEMESTER BTECH DEGREE PRACTICAL EXAMINATION**  
**FOR ELECTRONICS & COMMUNICATION ENGINEERING & COMPUTER SCIENCE AND**  
**ENGINEERING NOVEMBER 2025- 2020 SCHEME**

**UNIVERSITY COLLEGE OF ENGINEERING KARIAVATTOM**  
**Regular(2023 Admission)/Supplementary (2020 Admission To 2022 Admission)**

| Course                                        | Subject                                                 | Date                                                                                               | Centre                                         |
|-----------------------------------------------|---------------------------------------------------------|----------------------------------------------------------------------------------------------------|------------------------------------------------|
| Electronics & Communication Engineering (412) | ECL 331 - Analog Integrated Circuits and Simulation Lab | 29.01.2026 FN & 30.01.2026 FN                                                                      | University College of Engineering, Kariavattom |
|                                               | ECL 333 - Digital Signal Processing Lab                 | 02.02.2026 FN & 03.02.2026 FN                                                                      |                                                |
| Computer Science and Engineering(415)         | CSL 331: System Software & Microprocessors Lab          | 28.01.2026 FN<br>29.01.2026 FN<br>30.01.2026 FN<br>05.02.2026 FN<br>06.02.2026 FN & 09.02.2026 FN  |                                                |
|                                               | CSL 333: Database Management Systems Lab                | 28.01.2026 FN<br>29.01.2026 FN<br>30.01.2026 F N<br>05.02.2026 FN<br>06.02.2026 FN & 09.02.2026 FN |                                                |

Thiruvananthapuram  
Dated 16 .01. 2026

Sd/-  
**Controller of Examinations**